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FOREIGN PATENT DOCUMENTS							
Exa. m. Initi al*	Cite No. ¹	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴ Kind Code ⁵ (If known)				
P1		WO 02 17555	A	Verisign, Inc.	02/28/2002		
1		WO 01 75564	A	Intel Corporation	10/11/2001		
✓		WO 02 086684	A	Hewlett Packard	10/31/2002		
T4		JP02000076139	A	Nippon	03/14/2000		

Examiner Signature	Tuan V. Thai	Date Considered 09/14/09
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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Substitute for Form 1449A/PTO (Modified)			<i>Complete if Known</i>		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(use as many sheets as necessary)</small>			Application Number	10/041,071	
			Filing Date	02/28/2001	
			First Named Inventor:	Glew, et al.	
			Group Art Unit	2186	
			Examiner Name	Tuan V. Thai	
			Attorney Docket Number	042390.P13769	
OTHER ART – NO PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
Th.		"Information Display Technique for a Terminate Stay Resident Program," IBM Technical Disclosure Bulletin, TDB-ACC-No. NA9112156, 12/1/91, pp. 156-158, Vol. 34, Issue No. 7A			
Th.		ROBIN, JOHN SCOTT and IRVINE, CYNTHIA E., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor," Proceedings of the 9th USENIX Security Symposium, 8/14/2000, pp. 1-17, XP002247347, Denver, CO.			
Th.		KARGER, PAUL A., et al., "A VMM Security Kernel for the VAX Architecture," Proceedings of the Symposium on Research in Security and Privacy, 5/7/1990, pp. 2-19, XP010020182, ISBN: 0-8186-2060-9, Boxborough, MA.			
Th.		CHIEN, ANDREW A., et al., "Safe and Protected Execution for the Morph/AMRM Reconfigurable Processor," 7th Annual IEEE Symposium, FCCM '99 Proceedings 4/21/1999, pp. 209-221, XP010359180, ISBN: 0-7695-0375-6, Los Alamitos, CA.			
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